2161

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		Docket Number: **02885/93**	
Application Number **10/523,764**	Filing Date **Aug. 2, 2005**	Examiner **To be assigned**	Art Unit **2161**
Invention Title **DATA PROCESSING METHOD AND DEVICE**		Inventor **Martin VORBACH et al.**	

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on

Date: 10 Nov 2006

Signature: Michelle Carnivano Reg. No 36,098

SIR:

Pursuant to 37 CFR § 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The filing of this Information Disclosure Statement and the enclosed PTO Form No. 1449, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b). The paragraphs marked below are applicable. It is believed that no fees other than those indicated below are due, but authorization is hereby given to charge any additional fees due, or to credit any overpayment, to **Kenyon & Kenyon LLP, deposit account 11-0600**.

☒ 1. This Information Disclosure Statement is being filed (a) within three months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. §1.53(d), (b) within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, (c) before the mailing date of a first Office Action on the merits in the present application, OR (d) before the mailing of a first office action after filing of a request for continued examination. No certification or fee is required.

☒ 2. English-language Abstract of the non-English language reference is attached hereto where required.

☒ 3. Since this application was filed after June 30, 2003, copies of any cited U.S. references are not included.

Respectfully submitted,

Dated: 10 Nov 2008

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CUSTOMER NUMBER

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO-1449

Attorney Docket No.	Serial No.
2885/93	10/523,764
Applicant(s)	
VORBACH et al.	
Filing Date	Group Art Unit
Aug. 2, 2005	2161

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,996,083	November 30, 1999	Gupta et al.			
	6,434,695	August 13, 2002	Esfahani et al.			
	6,542,844	April 1, 2003	Hanna			
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						YES	NO
	**0 726 532	August 14, 1996	EPO				
	8-44581	February 16, 1996	Japan				
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OTHER DOCUMENTS

(= was cited in earlier statement, but is re-cited to correct inadvertent errors)**

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	Applicant(s) VORBACH et al.	
	Filing Date Aug. 2, 2005	Group Art Unit 2161

	Jantsch, Axel et al., "A Case Study on Hardware/software Partitioning," Royal Institute of Technology, Kista, Sweden, April 10, 1994 IEEE, pp. 111-118.
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	**Mirsky, E. et al, "MATRIX: A Reconfigurable Computing Architecture with Configurable Instruction Distribution and Deployable Resources," Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, 1996, pp. 157-166.
	Shirazi, et al., "Quantitative analysis of floating point arithmetic on FPGA based custom computing machines," IEEE Symposium on FPGAs for Custom Computing Machines, IEEE Computer Society Press, April 19-21, 1995, pp. 155-162.
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EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.